

## SECTION XV

### DISCIPLINED HIGH STABILITY QUARTZ OSCILLATOR

1-20-99

#### 1-1 INTRODUCTION

1-2 This internal oscillator option offers the user a highly accurate frequency source with excellent short-term stability and very good holdover performance during periods when GPS satellites are not usable. While GPS satellite data is available, the microprocessor controls this oscillator via a DAC in such a way as to phase lock its output to UTC. Since all time and frequency outputs of the clock are coherent with this oscillator, all display the same enhanced short-term stability and holdover performance.

1-3 This option consists of a 5 or 10 MHz high stability ovenized quartz housed in an ultra-compact package. The crystal provides excellent re-stabilization and re-trace characteristics, very low drift rate, and excellent short-term stability. The oven design provides excellent temperature stability.

#### 1-4 SPECIFICATIONS

1-5 All standard clock accuracy and stability specifications (see Section I) are valid when the clock has been operating continuously for at least 24 hours with an accurate position (error <10 m in WGS-84) and at least four satellites are visible. The ambient temperature variations within the specified operating temperature range must be less than 5°C. The addition of this option impacts the standard clock specifications in these areas:

1-6 Time Domain Stability (Allan Deviation):

$\tau$	$\sigma_y(\tau)$
1	$1 \times 10^{-11}$
$10^2$	$2 \times 10^{-11}$
24H	$1 \times 10^{-12}$

1-7 Holdover Characteristics (when not tracking satellites):

Initial Frequency Error:	Better than $1 \times 10^{-12}$ (after 1 day)
Drift Rate:	$1 \times 10^{-10}/\text{day}$
Temperature Coefficient:	$< 1 \times 10^{-10}/^\circ\text{C}$
Oscillator Stability:	$< 2 \times 10^{-9}$ , over 0°C to 50°C

1-8 Oscillator Warm-Up:  $< 15$  minutes to  $1 \times 10^{-7}$

#### 2-1 INSTALLATION

2-2 This option is factory installed. Field installation is not available for this option.

#### 3-1 OPERATION

3-2 No special operation procedures are required, however initial acquisition of satellites after a cold start-up will be delayed due to the warm-up time of the ovenized oscillator.

#### 4-1 THEORY OF OPERATION

4-2 The clock provides accurate time and frequency whenever one or more satellites are in view, with optimal performance when four or more satellites are in view. When satellite outages do occur, the clock flywheels on its oscillator, either internal or, when using the External Oscillator Control option, external oscillator. During these periods, the rate that the clock time and frequency outputs diverge from UTC is governed by these parameters:

- 1) The accuracy of the last DAC control voltage setting prior to the outage.
- 2) The ambient temperature change during the outage period and the temperature coefficient of the oscillator output frequency.
- 3) The inherent drift or aging rate of the oscillator's output frequency as a function of time. All quartz oscillators exhibit this drift.

Items 2) and 3) are functions of the quartz oscillator and the temperature characteristics of the environment in which the clock is operated. Item 1) is determined by the stability of the GPS system and the control parameters chosen in the digital phase lock loop implemented in the microprocessor.

4-3 The oscillator control algorithm employed in the clock implements a Type III servo on the phase relationship of the clock to UTC as measured via GPS clock bias solutions performed in the core GPS module. Proprietary algorithms are employed to affect multi-satellite averaging and to detect and remove data outliers so that optimally stable steering data is applied to the oscillator. The output of the control algorithm is a DAC generated voltage which is connected to the Electronic Tuning Control input of the oscillator.

The control philosophy is to implement sufficient averaging to reduce the short term effects on stability caused by Selective Availability while maintaining the long term stability available from the GPS system. This philosophy requires that trade-offs be made between having better short and medium term stability versus having better insensitivity to environmentally induced instability, e.g. temperature induced oscillator frequency shifts which cannot be eliminated when heavy control loop averaging is in effect.

The parameters which are fixed in clock EPROM firmware assume an air conditioned environment with night to day variations on the order of 5°C. Under these conditions all time and frequency performance specifications of the clock will be maintained. Larger variations in temperature may induce out of specification performance.

For users whose operating environment differs significantly from the assumed environment, the available clock External Oscillator Control option offers the ability to tailor the oscillator control parameters to the user's environment. This option is described fully in Section III in keypad or Serial I/O Functions 07 and 14 and is primarily intended for users who wish to supply their own oscillators for control by the clock. However the operation with an optional internal ovenized quartz oscillator such as this one is identical in function. Keypad or Serial I/O Function 14 allows user oscillator parameter set-up and Function 07 allows enabling of *internal* oscillator control using these user defined parameters in exactly the same way as they would be used for a *user provided external* oscillator. The one disadvantage of this method of defining oscillator control parameters is that they are not stored in firmware. Failure of the battery backed NVRAM would cause the clock to revert to the default firmware resident parameters, requiring user intervention to re-program the "forgotten" parameters and re-enable them.

Should operation via the External Oscillator Control option be desired, these Function 14 parameters would yield equivalent operation as compared to the firmware resident parameters:

### 5 MHz High Stability OCXO

Freq:	5
Tuning Slope:	-6.00e-08
DAC Nominal:	0.50
Temp Stab:	3.00e-09

### 10 MHz High Stability OCXO

Freq:	10
Tuning Slope:	1.00e-07
DAC Nominal:	0.50
Temp Stab:	3.00e-09

## 5-1 MAINTENANCE

5-2 There is no maintenance required for this option. Over many years time, the oscillator frequency will drift out of the control range of the electronic tuning. The combination of very low aging rate and wide tuning range of the oscillator will keep this event from occurring before at least 10 years of continuous operation. By observing the DAC value returned from either keypad or Serial I/O Function 71 while the clock is properly locked to the GPS system, the user may determine when replacement of the oscillator is required.

Since the DAC value returned by Function 71 is a signed 16 bit integer representation of the DAC output voltage, the positive and negative limits are at 32767 and -32768, respectively. Whenever the clock is operating properly with locked status and the steady state DAC value is greater than 30000 or less than -30000, planning for replacement of the oscillator is indicated. The unit should be returned to the factory for performance of this procedure.